

Figure 1

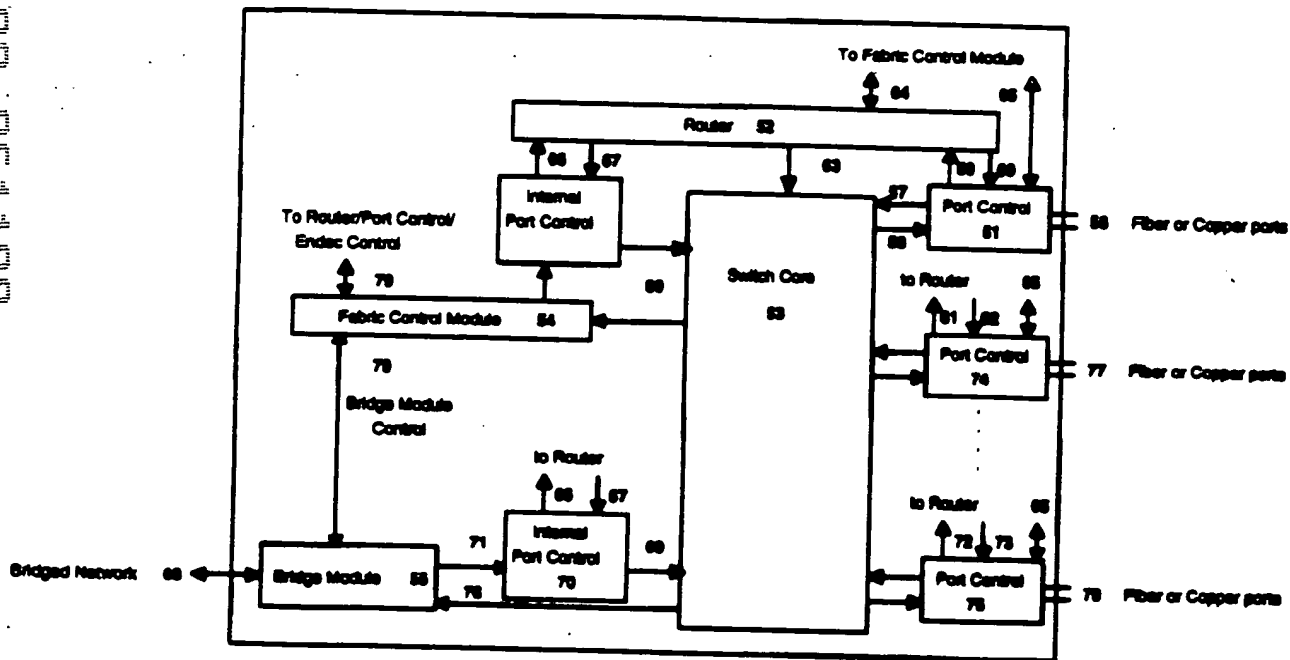


Figure 2

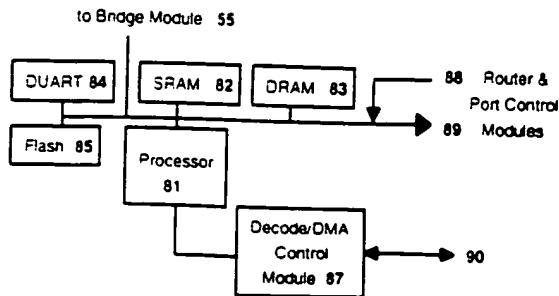


Figure 3

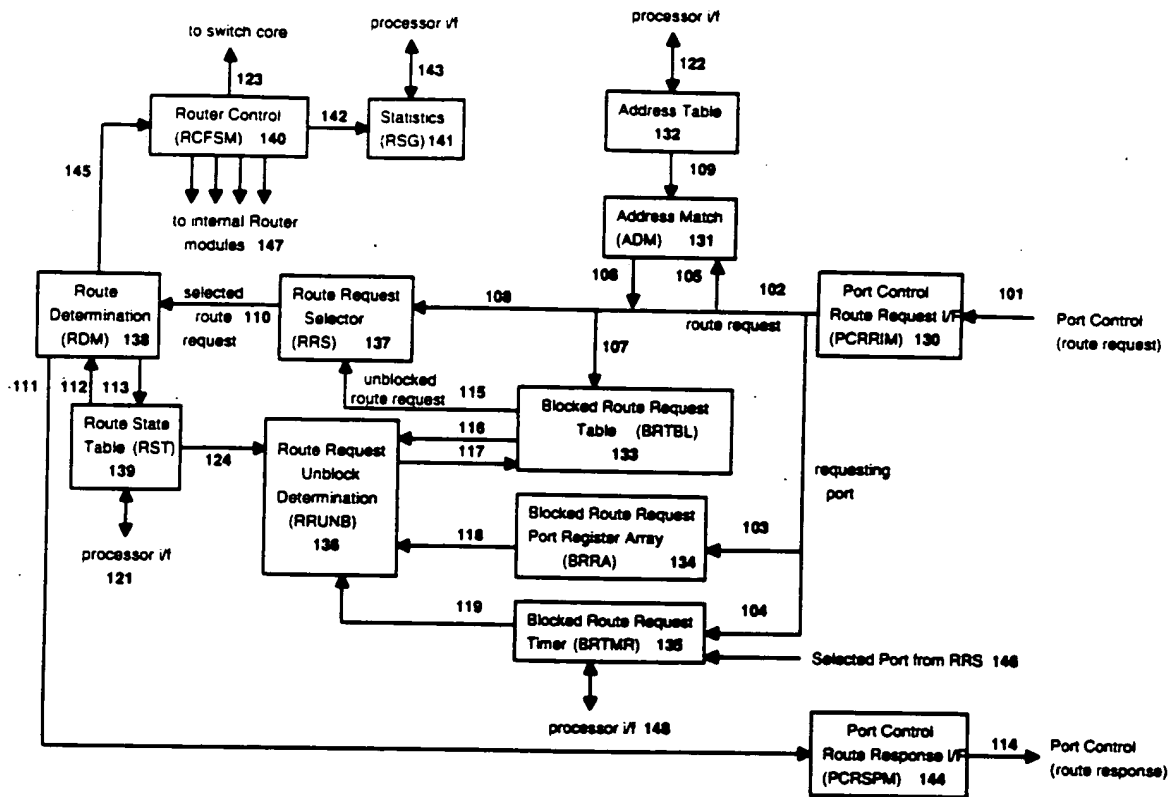


Figure 4

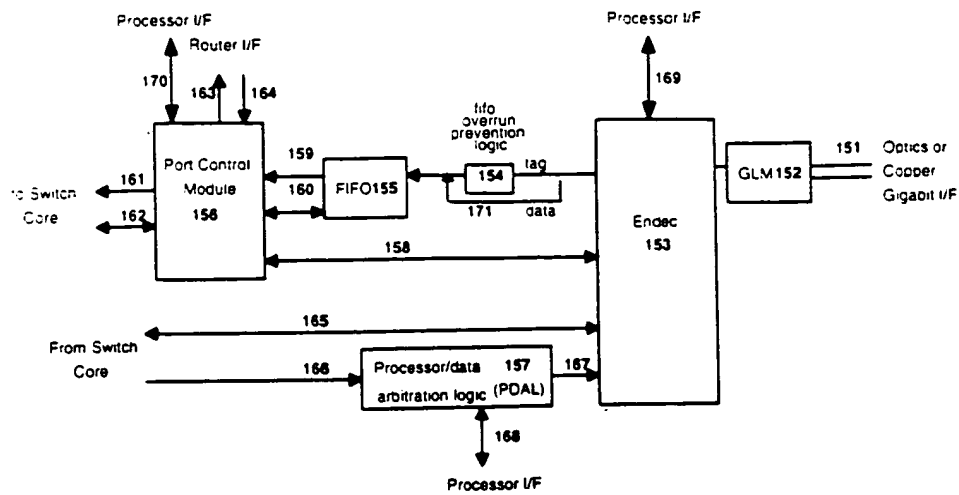


Figure 5

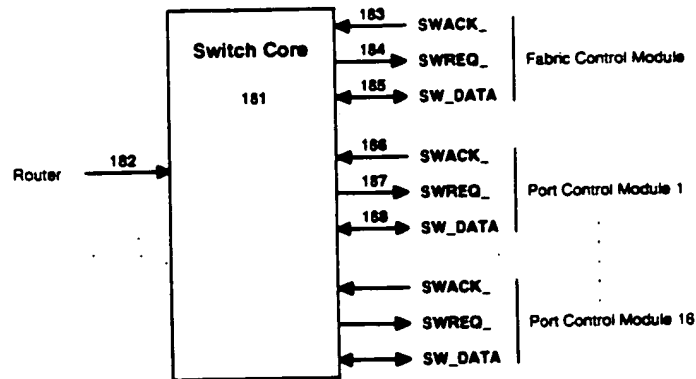


Figure 6

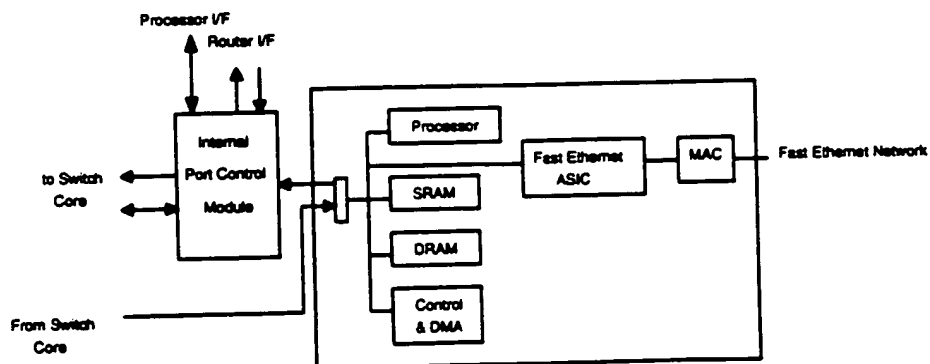


Figure 7

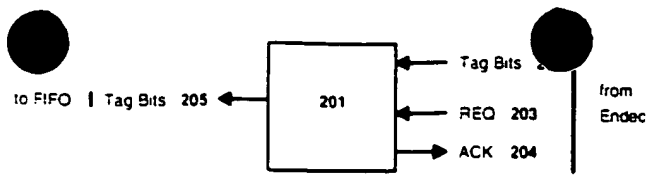


Figure 8

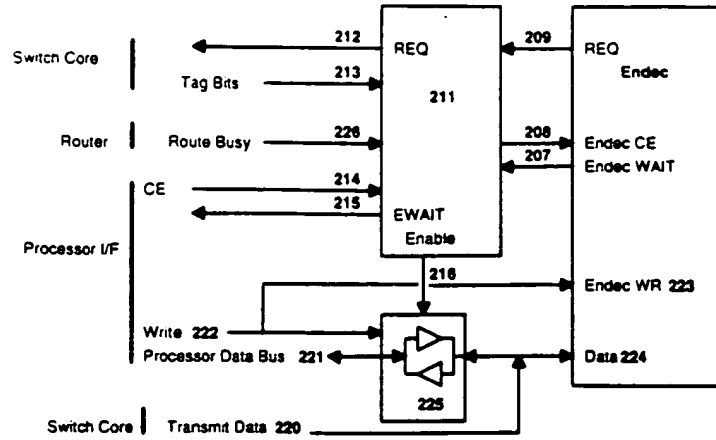


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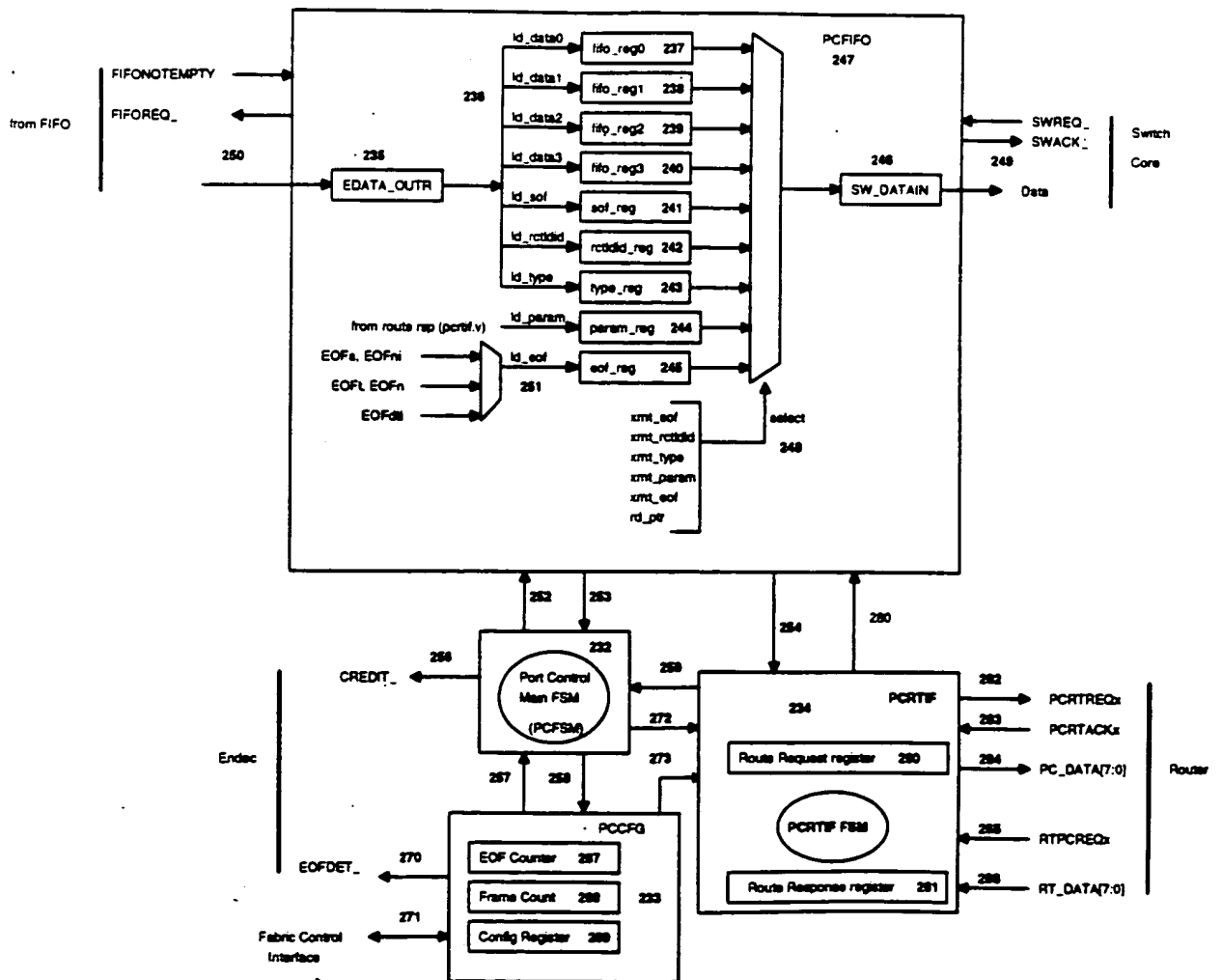


Figure 10

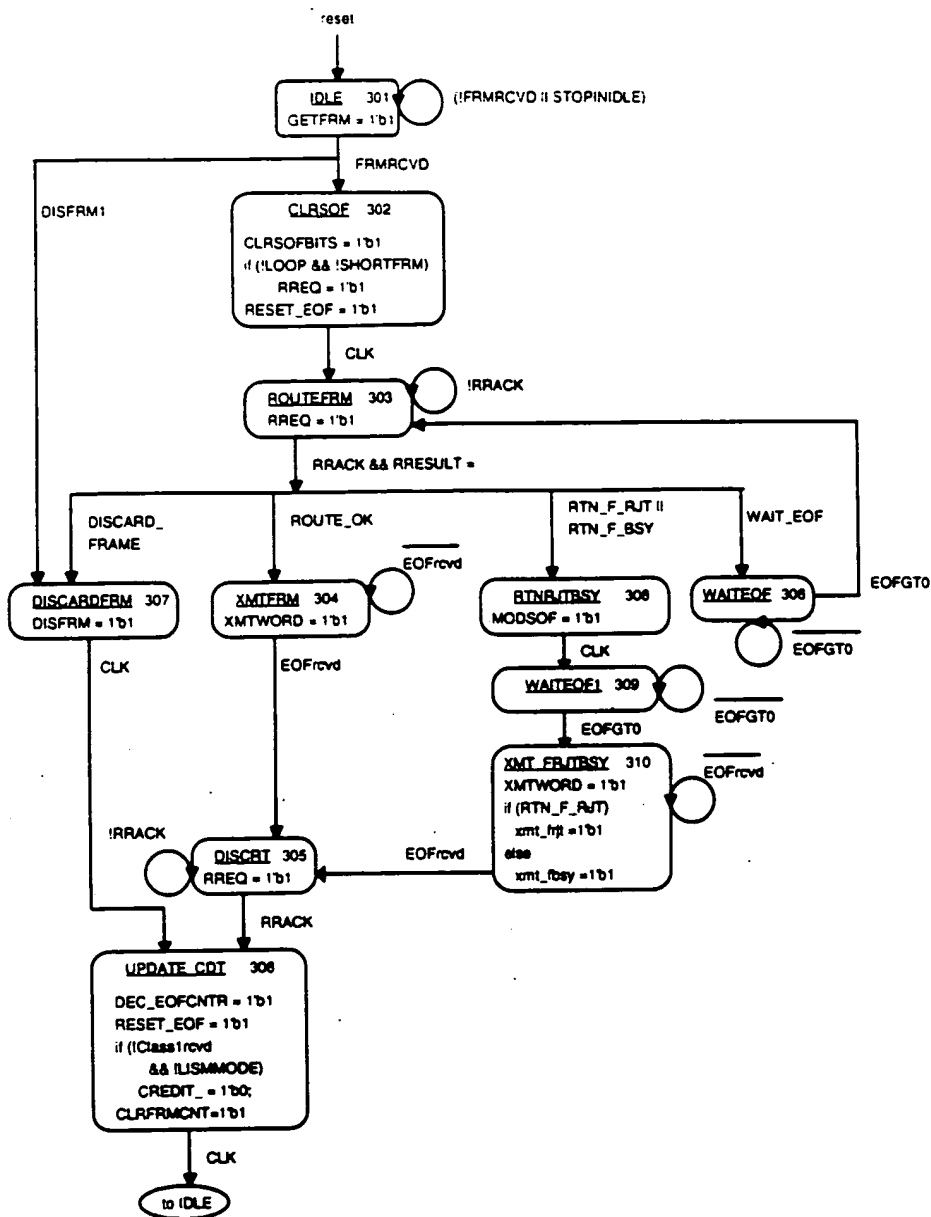


Figure 11

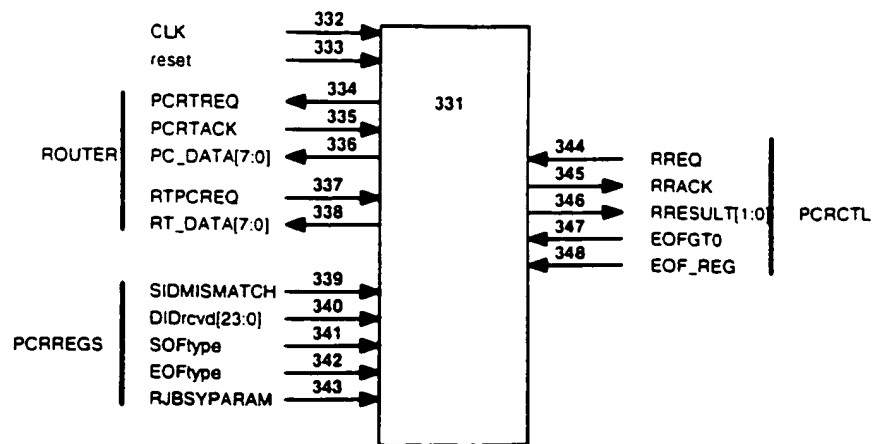


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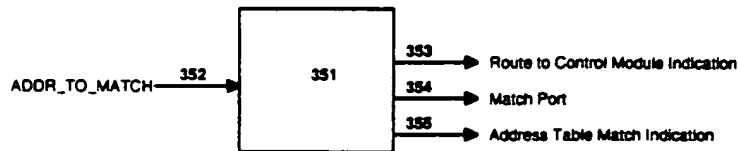


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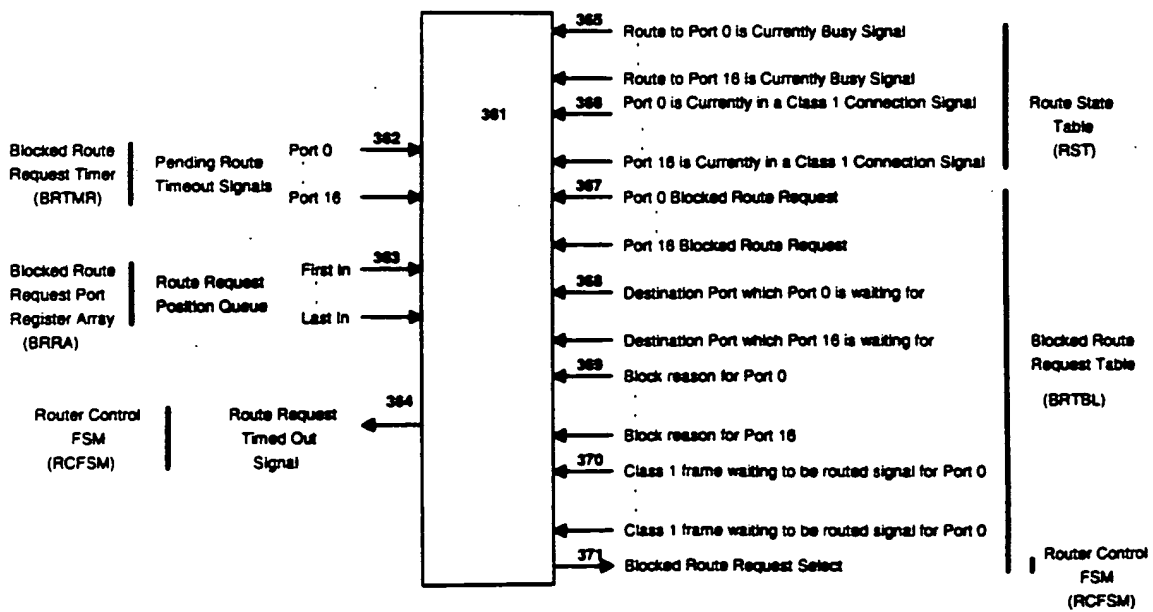


Figure 14

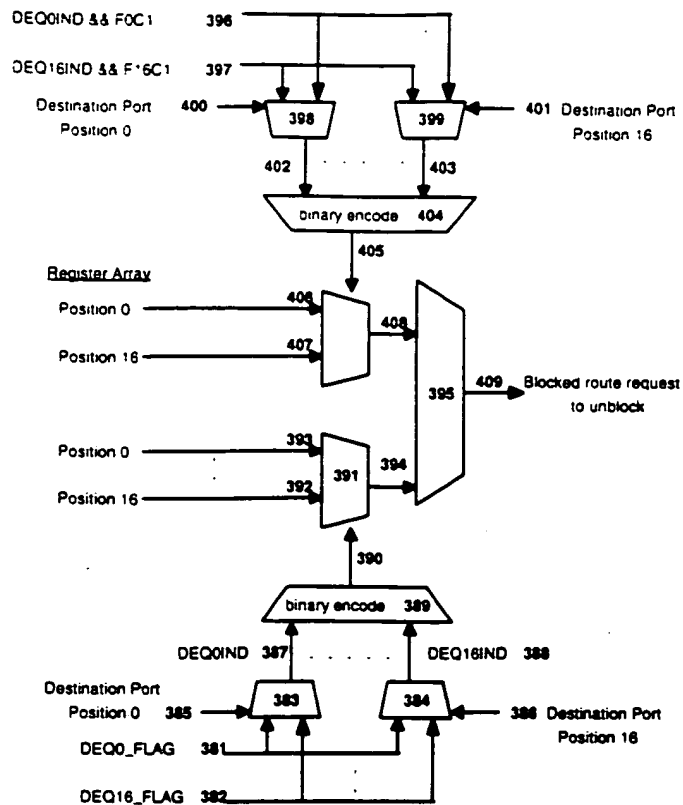


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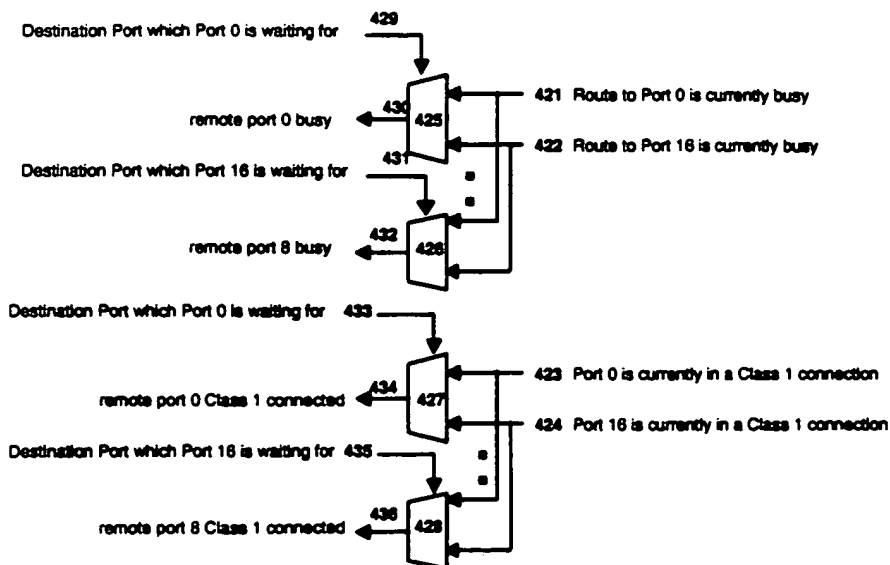


Figure 16

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State Table

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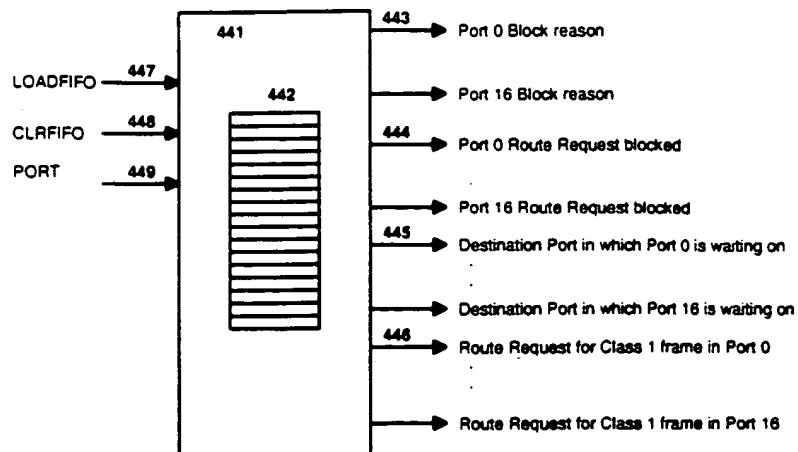


Figure 17

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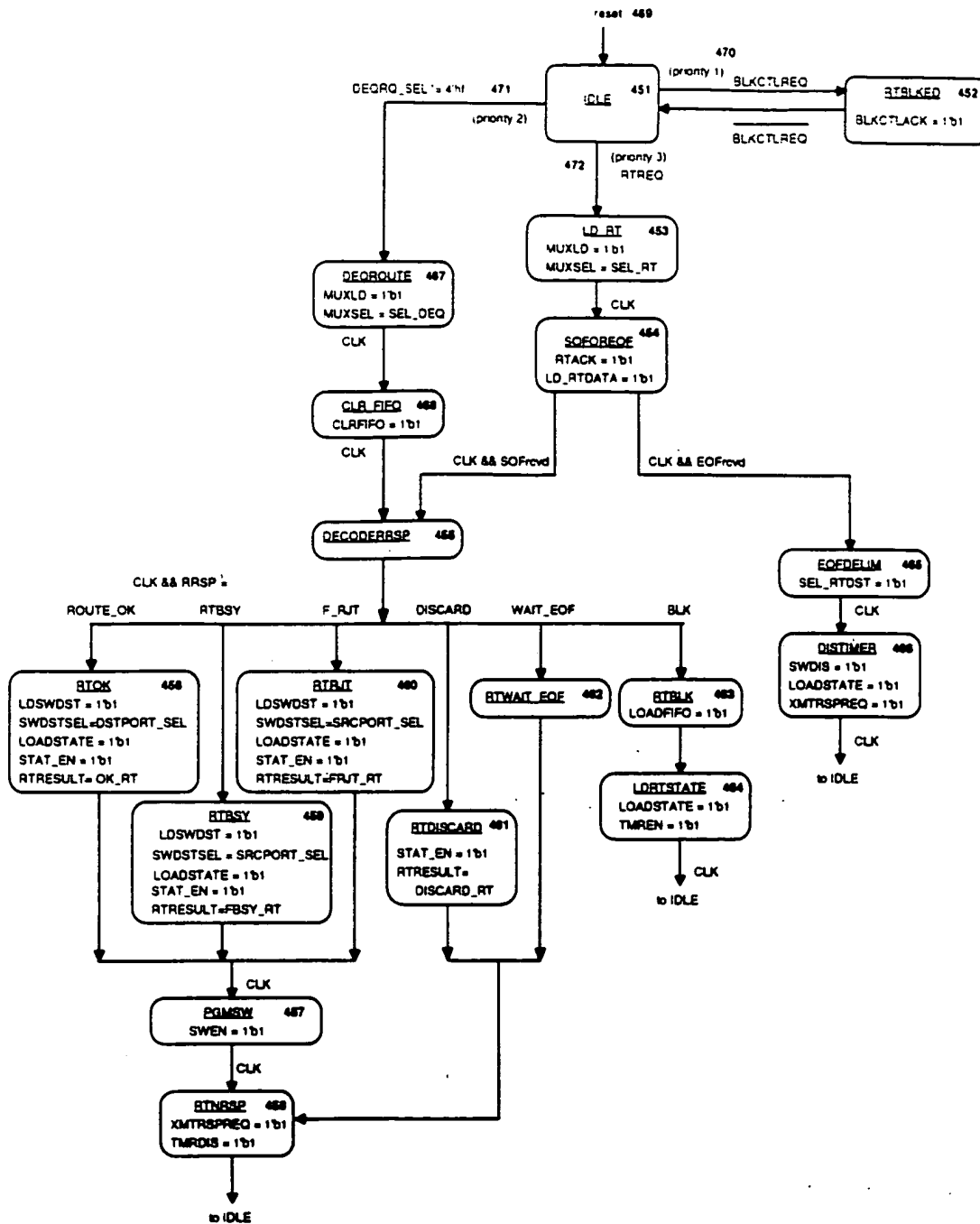


Figure 18

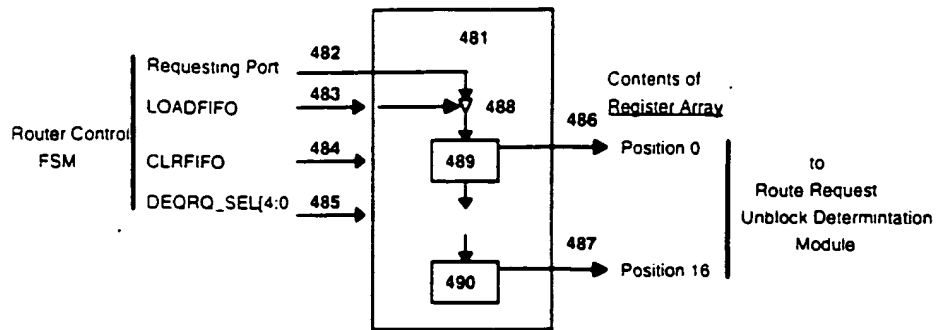


Figure 19

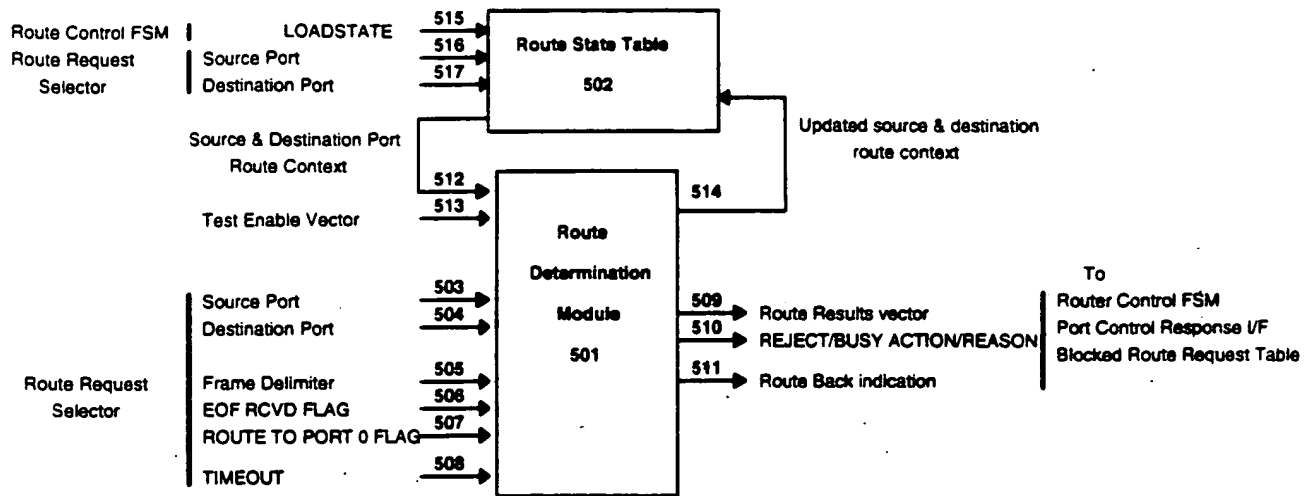


Figure 20

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Route Determination Module 521

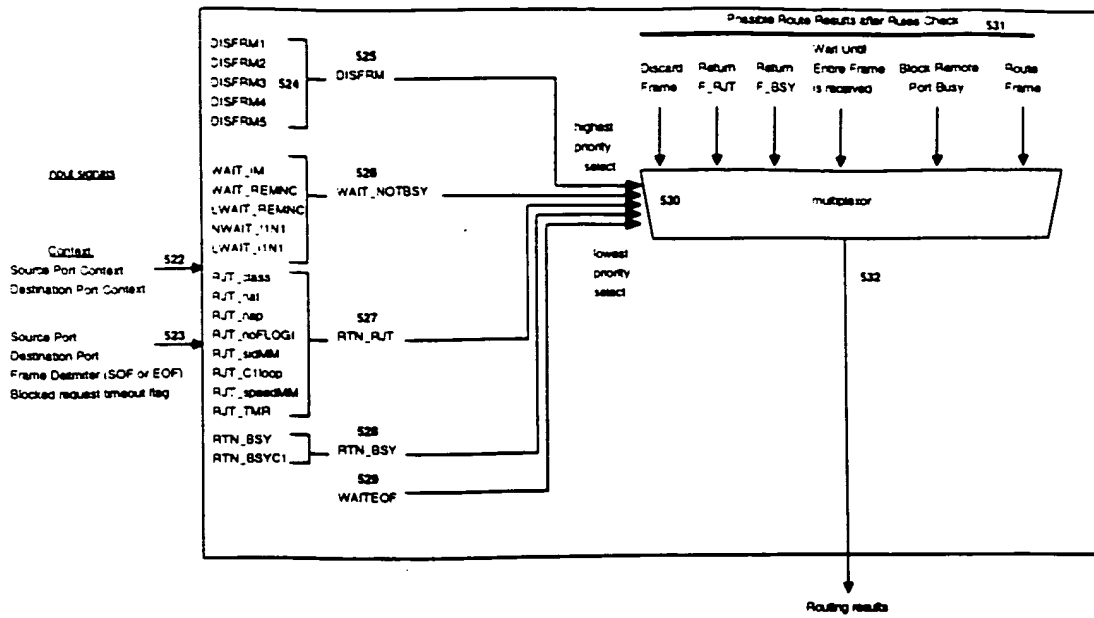


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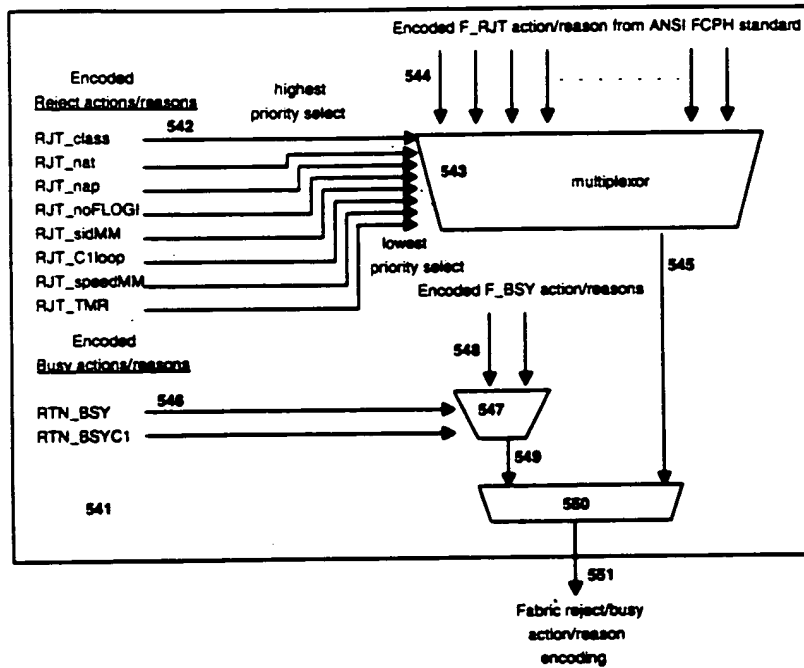


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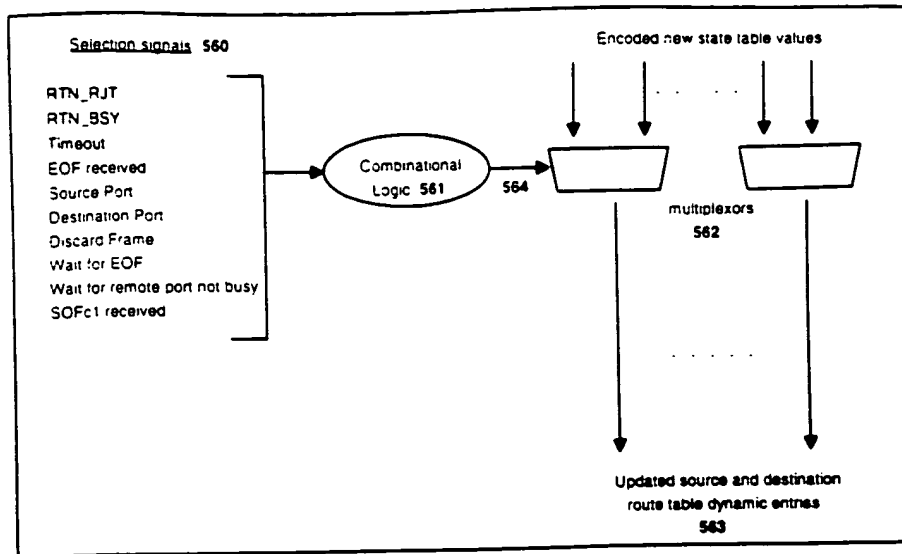


Figure 23

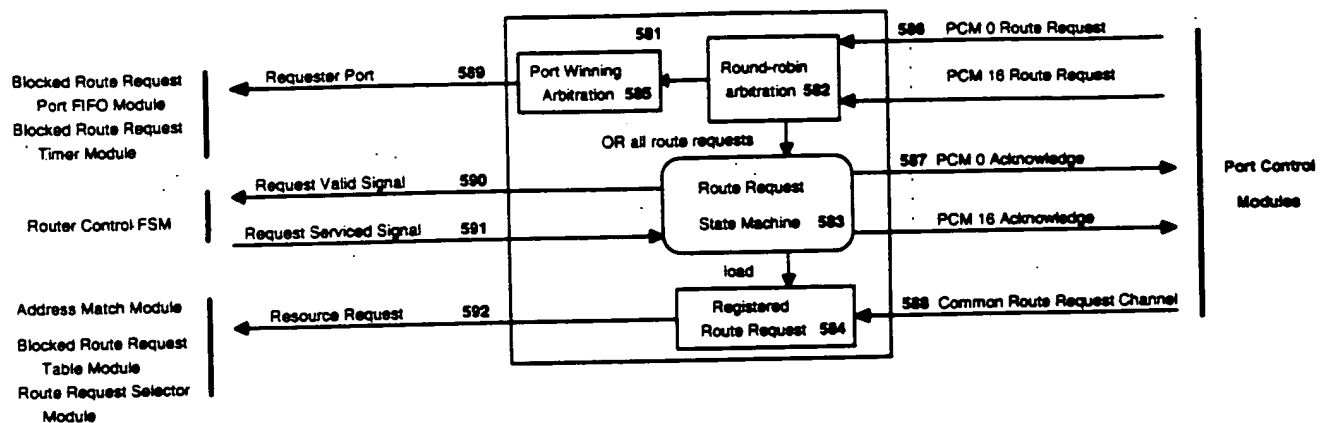


Figure 24

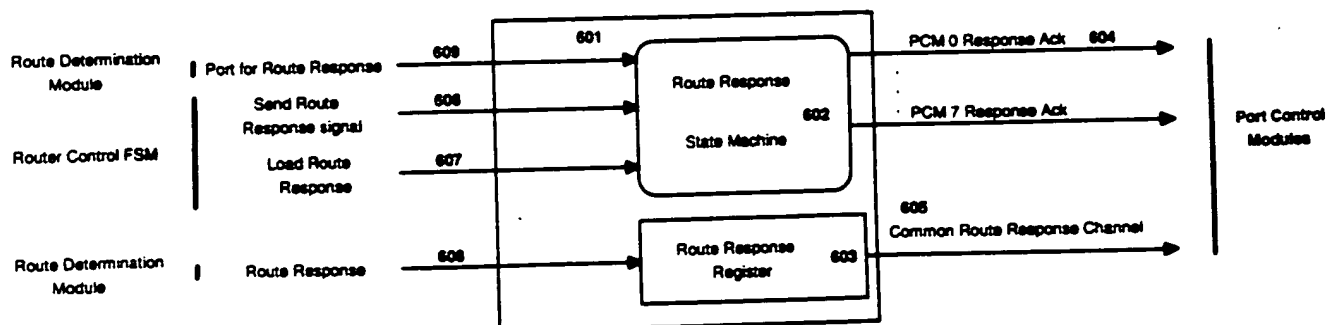


Figure 25

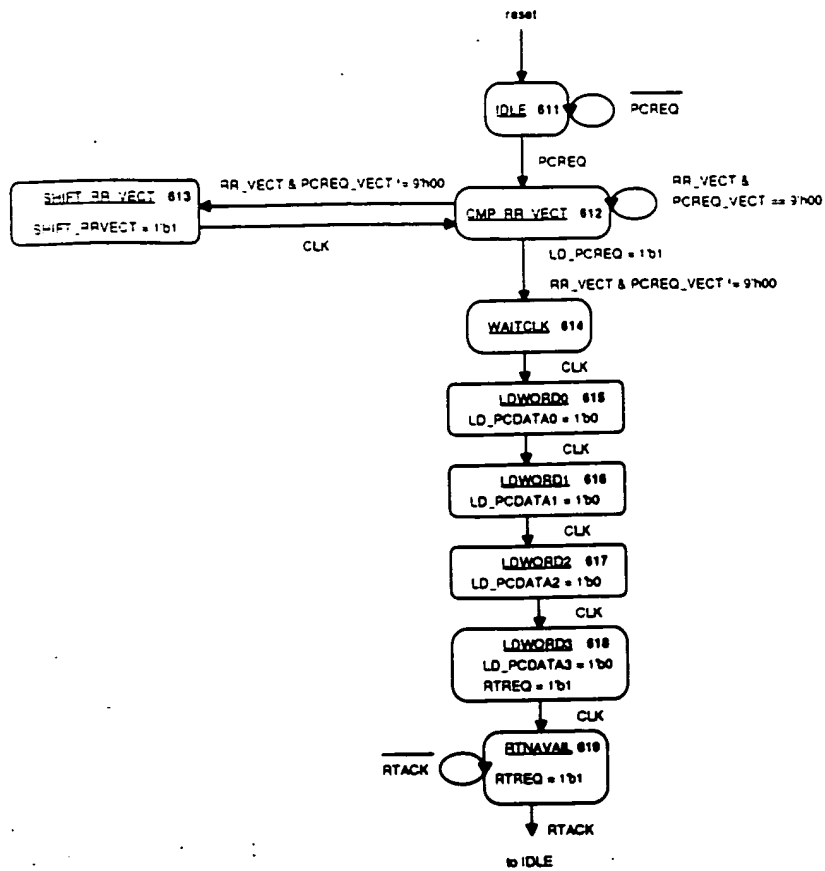


Figure 26

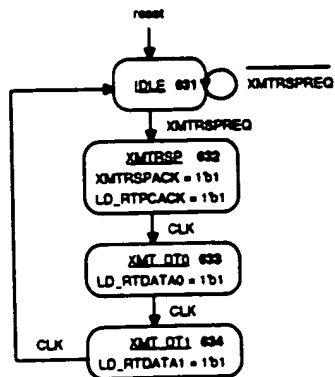


Figure 27

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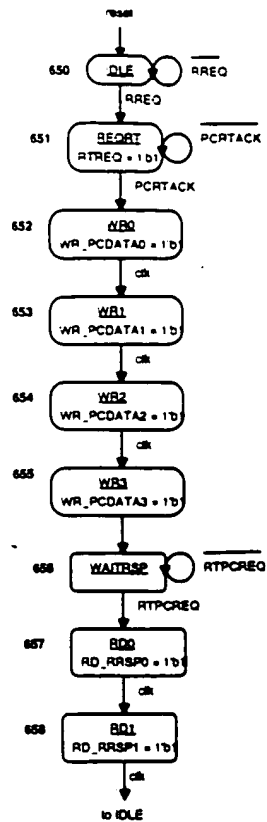


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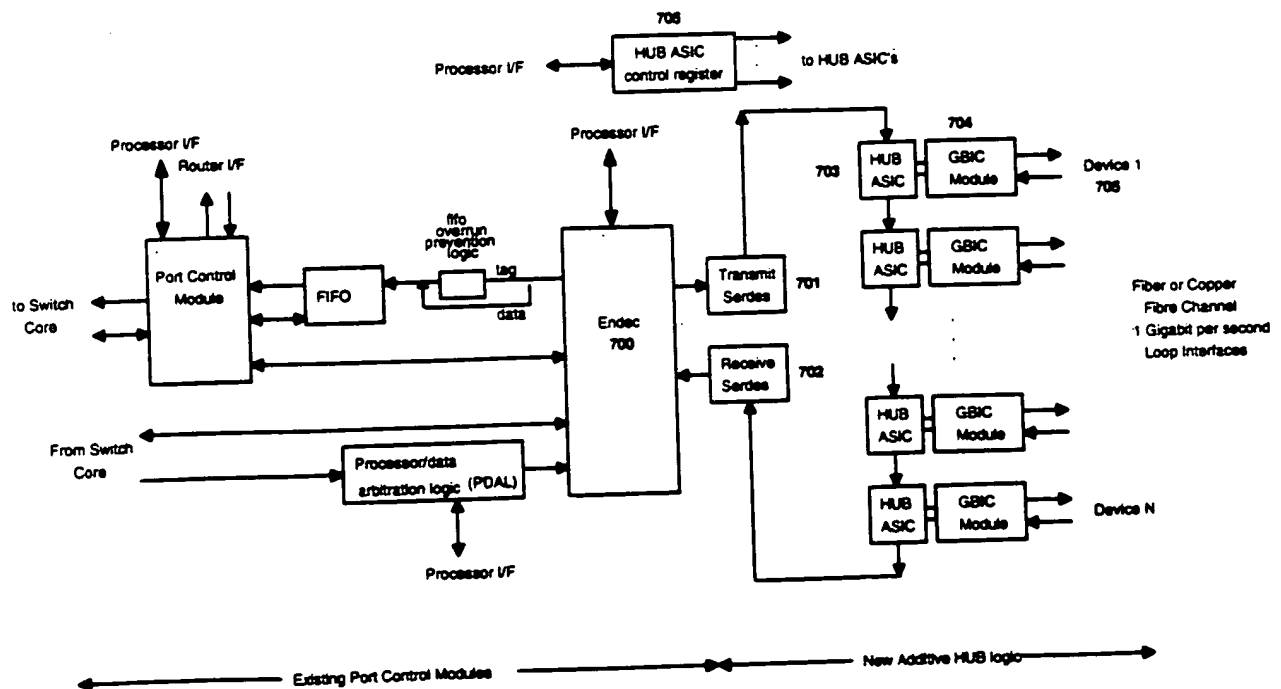


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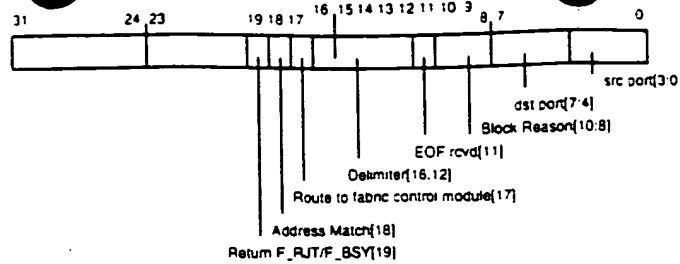


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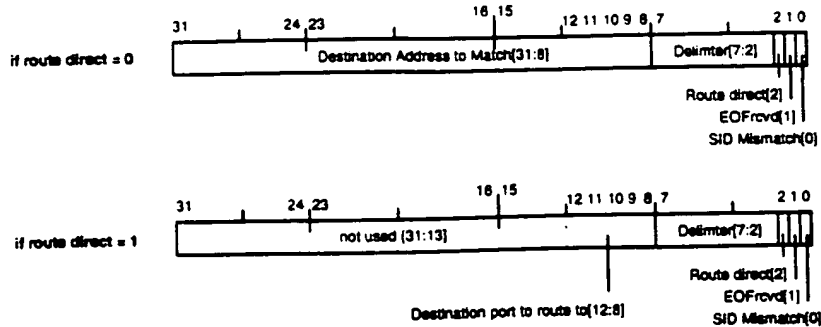


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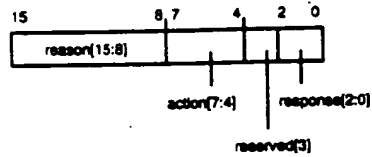


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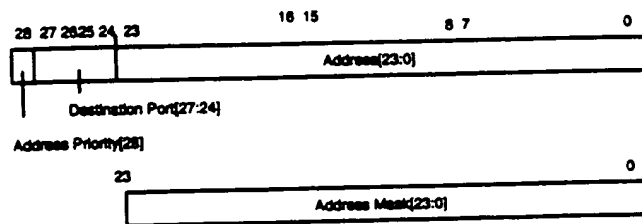


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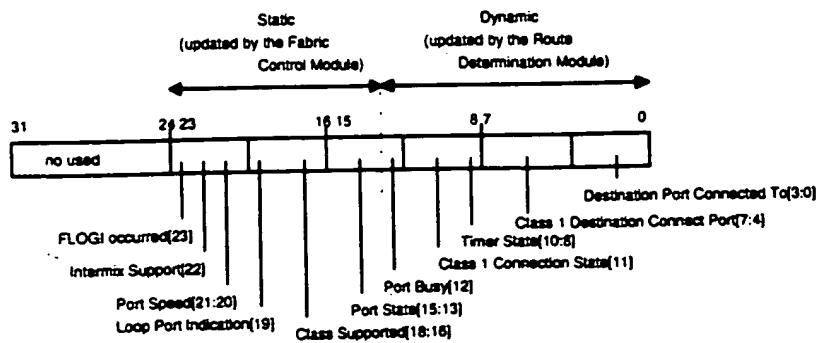


Figure 34

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